

Current Transducer LH 25-NP

 I_{PN} = 8, 12, 25 A

For the electronic measurement of current: DC, AC, pulsed..., with galvanic separation between the primary and the secondary circuit.





Features

- Closed loop (compensated) current transducer
- PCB mounting
- Current output.

Advantages

- High accuracy
- Very low offset
- Very low offset drift over temperature.

Applications

- AC variable speed and servo motor drives
- Static converters for DC motor drives
- Battery supplied applications
- Uninterruptible Power Supplies (UPS)
- Switched Mode Power Supplies (SMPS)
- Power supplies for welding applications
- · Solar inverters.

Standards

• IEC 61800-1: 1997

• IEC 61800-2: 2015

• IEC 61800-3: 2004

• IEC 61800-5-1: 2007

• IEC 62109-1: 2010

• IEC 62477-1: 2012

• UL 508: 2013.

Application Domain

Industrial.



Absolute maximum ratings

Parameter	Symbol	Unit	Value
Maximum supply voltage (working) (-40 85 °C)	$U_{\rm C\; max}$	V	±15.75
Maximum primary conductor temperature	$T_{ m B\ max}$	°C	100
Maximum steady state primary current (-40 85 °C)	I_{PNmax}	А	25

Stresses above these ratings may cause permanent damage.

Exposure to absolute maximum ratings for extended periods may degrade reliability.

UL 508: Ratings and assumptions of certification

File # E189713 Volume: 2 Section: 10

Standards

- USR indicates investigation to the Standard for Industrial Control Equipment UL 508.
- CNR indicates investigation to the Canadian standard for Industrial Control Equipment CSA C22.2 No. 14-13

Conditions of acceptability

When installed in the end-use equipment, with primary feed through potential involved of 600 V AC/DC, consideration shall be given to the following:

- 1 These products must be mounted in a suitable end-use enclosure.
- 2 The secondary pin terminals have not been evaluated for field wiring.
- 3 Low voltage control circuit shall be supplied by an isolating source (such as transformer, optical isolator, limiting impedance or electro-mechanical relay).
- 4 Based on the temperature test performed on all series, the primary bar or conductor shall not exceed 100 °C in the end use application.

Marking

Only those products bearing the UL or UR Mark should be considered to be Listed or Recognized and covered under UL's Follow-Up Service. Always look for the Mark on the product.



Insulation coordination

Parameter	Symbol	Unit	Value	Comment
RMS voltage for AC insulation test, 50 Hz, 1 min	U_{d}	kV	5	
Impulse withstand voltage 1.2/50 μs	U_{Ni}	kV	12	
Insulation resistance	R_{INS}	GΩ	≥ 1	measured at 500 V DC
Partial discharge extinction RMS voltage @ 10 pC	U_{e}	kV	> 2	
Clearance (pri sec.)	d_{CI}	mm	See dimensions	
Creepage distance (pri sec.)	d_{Cp}	mm	drawing on page 11	
Case material	-	-	V0	According to UL 94
Comparative tracking index	CTI		600	
Application example		V	600	Reinforced insulation non uniform field according to IEC 61800-5-1, CAT III, PD 2
Application example		V	1000	Basic insulation non uniform field according to IEC 61800-5-1, CAT III, PD 2

Environmental and mechanical characteristics

Parameter	Symbol	Unit	Min	Тур	Max	Comment
Ambient operating temperature	T_{A}	°C	-40		85	
Ambient storage temperature	T_{Ast}	°C	-50		90	
Mass	m	g		19		



Electrical data

At $T_{\rm A}$ = 25 °C, $\pm U_{\rm C}$ = ± 15 V, $R_{\rm M}$ = 1 Ω , unless otherwise noted. Lines with a * in the conditions column apply over the -40 ... 85 °C ambient temperature range

Parameter	Symbol	Unit	Min	Тур	Max		Comment	
Primary nominal RMS current	I_{PN}	Α		25		*		
Primary current, measuring range	I_{PM}	Α	-55		55	*	With $R_{\rm M} \le 148 \ \Omega$ with $U_{\rm C} = \pm 15 \ {\rm V}$	
Measuring resistance	R_{M}	Ω	68 ¹⁾			*	Measuring resistance range is defined in figures 1, 2	
Secondary nominal RMS current	I_{SN}	mA		25		*		
Resistance of secondary winding	$R_{\rm S}$	Ω			65		$R_{\rm S}(T_{\rm A})$ = $R_{\rm S}$ ×(1+0.004×($T_{\rm A}$ + Δ temp-25)) Estimated temperature increase @ $I_{\rm PN}$ is Δ temp = 15 °C	
Secondary current	I_{S}	mA	-55		55	*		
Number of secondary turns	N_{S}			1000				
Theoretical sensitivity		mA/A		1				
Supply voltage	$\pm U_{\rm C}$	V	±11.4		±15.75	*		
Current consumption	I_{C}	mA		20 21			$\pm U_{\rm c} = \pm 12 \text{ V}$ $\pm U_{\rm c} = \pm 15 \text{ V}$	
Offset current referred to primary	I	Α	-0.1		0.1			
Offset current referred to secondary	I_{\circ}	mA	-0.05		0.05			
Overall <i>I</i> _o at −40 85° C, referred to primary	Ī	А	-0.15		0.15	*		
Overall I_0 at -40 85° C, referred to secondary	$ar{I}_{ extsf{O} ext{ ov}}$	mA	-0.075		0.075	*		
Temperature coefficient of I_{OE} referred to primary	TCI _{OE}	μA/K	-400		400	*		
Magnetic offset current after $5 \times I_{PN}$ referred to secondary	$I_{\mathrm{O}\mathrm{M}}$	mA		±0.04			After 5 × I _{PN}	
Sensitivity error	ε_{S}	%	-0.2		0.2	*		
Temperature coefficient of S	TCS	ppm/K	-30		30	*		
Linearity error	ε_{L}	% of $I_{\rm PN}$	-0.1		0.1	*		
Total error at I_{PN}	$\varepsilon_{ m tot}$	%			0.4	*		
Error at I_{PN}	ε	%			0.2	*		
RMS noise current referred to primary	I_{no}	mA		23			1 Hz to 100 kHz (see fig. 4)	
Delay time to 10 % of the final output value for I_{PN} step	t _{D 10}	ns		200			0 to 25 A, 50 A/μs, $R_{\rm M}$ = 10 Ω	
Delay time to 90 % of the final output value for I_{PN} step	t _{D 90}	ns		500			0 to 25 A, 50 A/ μ s, $R_{\rm M}$ = 10 Ω (fig.3) PCB design defined in fig. 7	
Delay time to 90 % of the final output value for I_{PN} step	t _{D 90}	ns		100			0 to 25 A, 50 A/ μ s, $R_{\rm M}$ = 10 Ω ; PCB design defined in fig. 6	
Frequency bandwidth	BW	kHz		200			$R_{\rm M}$ = 10 Ω; -3 dB	

Note: 1) With ± 15 V ± 5 % as power supply, $T_{\rm A}$ = 85 °C and $I_{\rm PN}$ (DC value). Other values of minimum values according to conditions of use are given in Figures 1, 2.



Typical performance characteristics

Definition of typical, minimum and maximum values

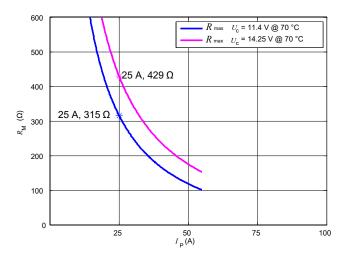
Minimum and maximum values for specified limiting and safety conditions have to be understood as such as well as values shown in "typical" graphs.

On the other hand, measured values are part of a statistical distribution that can be specified by an interval with upper and lower limits and a probability for measured values to lie within this interval.

Unless otherwise stated (e.g. "100 % tested"), the LEM definition for such intervals designated with "min" and "max" is that the probability for values of samples to lie in this interval is 99.73 %.

For a normal (Gaussian) distribution, this corresponds to an interval between -3 sigma and +3 sigma. If "typical" values are not obviously mean or average values, those values are defined to delimit intervals with a probability of 68.27 %, corresponding to an interval between -sigma and +sigma for a normal distribution.

Typical, maximal and minimal values are determined during the initial characterization of the product



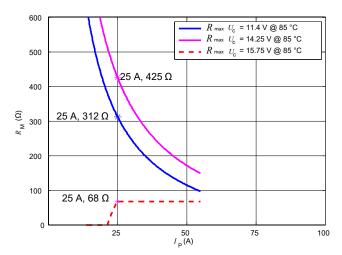


Figure 1: Maximum measuring resistance @ 70 °C in DC

Figure 2: Maximum measuring resistance @ 85 °C in DC

$$R_{\text{M max}} = N_{\text{S}} \times \frac{U_{\text{C min}} - 1.5 \text{ V}}{I_{\text{D}}} - R_{\text{S max}}$$

The measuring resistance $(R_{\rm M})$ has to be properly defined in order to guarantee the optimal transducer performance. Maximum measuring resistance $(R_{\rm M\ max})$ is based on the maximum current that must be measured by the transducer. The simple formula $R_{\rm M}$ is shown above.

The simple formula $R_{\rm M\ max}$ is shown above. Minimum measuring resistance ($R_{\rm M\ min}$) is based on the maximum heat dissipation capacity of the transducer. It depends on the following working conditions: supply voltage $U_{\rm C}$, ambient temperature $T_{\rm A}$ and the nominal primary RMS current ($I_{\rm P\ N}$).

Minimum $R_{\rm M}$ value can be as low as 0 Ω , and this is normally the case when the transducer is supplied with a low supply voltage $U_{\rm C}$ = ±12 V.

In figure 1 the minimum $R_{\rm M}$ value data is omitted whenever it is equal to 0 Ω .



Typical performance characteristics continued

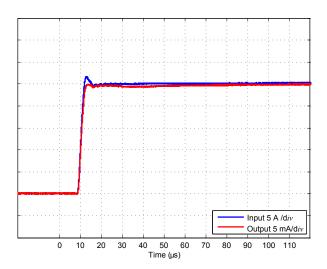


Figure 3: Typical delay time (0 to 25 A, 50 A/ μ s) with $R_{\rm M}$ = 10 Ω

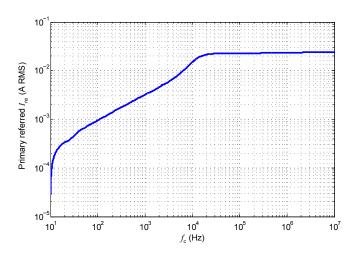


Figure 4: Typical total RMS noise current (primary referred) with $R_{\rm M}$ = 10 Ω

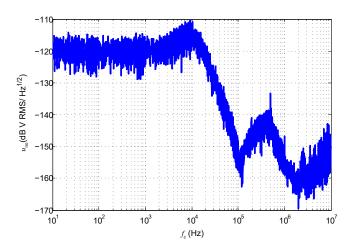


Figure 5: Typical noise voltage spectral density $u_{\rm no}$ with $R_{\rm M}$ = 10 Ω



High and low speed PCB designs

The PCB design is very important to achieve good response to high-frequency primary current in terms of accuracy and bandwidth. High speed PCB design, as the one shown here below, allows the transducer to obtain lower delay times to high varying primary current. In order to achieve the highest bandwidth possible the loop of the primary current traces at the transducer back should be as tight as possible.

On the other hand, high speed PCB design requires more PCB area for the primary current traces compared to low speed PCB design.

The customer must define the optimal PCB design according to the application specifications.

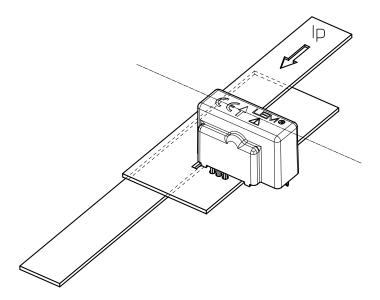


Figure 6: High speed PCB design

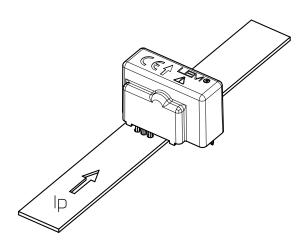


Figure 7: Low speed PCB design



Performance parameters definition

Ampere-turns and amperes

The transducer is sensitive to the primary current linkage $\Theta_{\scriptscriptstyle D}$ (also called ampere-turns).

$$\Theta_{P} = N_{P} \cdot I_{P}$$
 (At)

Where $N_{\rm p}$ is the number of primary turn (depending on the connection of the primary jumpers)

Caution: As most applications will use the transducer with only one single primary turn ($N_p = 1$), much of this datasheet is written in terms of primary current instead of current linkages. However, the ampere-turns (At) unit is used to emphasis that current linkages are intended and applicable.

Transducer simplified model

The static model of the transducer at temperature $T_{\scriptscriptstyle A}$ is:

$$I_{\rm S} = S \cdot \Theta_{\rm p} + \varepsilon$$

In which $\varepsilon =$

$$I_{\text{O E}} + I_{\text{O }T}(T_{\text{A}}) + \varepsilon_{\text{S}} \cdot \Theta_{\text{P}} \cdot S + \varepsilon_{\text{L}} \left(\Theta_{\text{P max}}\right) \cdot \Theta_{\text{P max}} \cdot S + TCS \cdot (T_{\text{A}} - 25) \cdot \Theta_{\text{P}} \cdot S$$

 $\Theta_{P} = N_{P} \cdot I_{P}$: primary current linkage (At)

: max primary current linkage applied to

the transducer

: secondary current (A)

: ambient operating temperature (°C) : electrical offset current (A)

: temperature variation of I_{Ω} at temperature

 T_{A} (°C)

S : sensitivity of the transducer (A/At) TCS

: temperature coefficient of S: sensitivity error

: linearity error for $\Theta_{ extst{P max}}$

This model is valid for primary ampere-turns $\Theta_{\scriptscriptstyle P}$ between $-\Theta_{\mathrm{P\,max}}$ and $+\Theta_{\mathrm{P\,max}}$ only.

Sensitivity and linearity

To measure sensitivity and linearity, the primary current (DC) is cycled from 0 to $I_{\rm p}$, then to $-I_{\rm p}$ and back to 0 (equally spaced $I_{\rm p}/10$ steps). The sensitivity S is defined as the slope of the linear regression line for a cycle between $\pm I_{PN}$.

The linearity error $\varepsilon_{\rm L}$ is the maximum positive or negative difference between the measured points and the linear regression line, expressed in % of I_{PN} .

Magnetic offset

The magnetic offset current I_{OM} is the consequence of a current on the primary side ("memory effect" of the transducer's ferromagnetic parts). It is measured using the following primary current cycle. I_{OM} depends on the current value $I_{P1}(I_{P1} > I_{PM})$.

$$I_{OM} = \frac{I_{S}(t_{1}) - I_{S}(t_{2})}{2} \cdot \frac{1}{S}$$

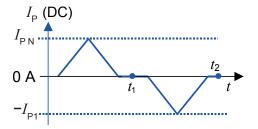


Figure 8: Current cycle used to measure magnetic and electrical offset (transducer supplied)



Performance parameters definition continued

Electrical offset

The electrical offset current I_{OE} can either be measured when the ferro-magnetic parts of the transducer are:

- completely demagnetized, which is difficult to realize,
- or in a known magnetization state, like in the current cycle shown in figure 8.

Using the current cycle shown in figure 8, the electrical offset is:

$$I_{\text{OE}} = \frac{I_{\text{S}}(t_{\text{1}}) + I_{\text{S}}(t_{\text{2}})}{2}$$

The temperature variation $I_{\rm O\ T}$ of the electrical offset current $I_{\rm O\ E}$ is the variation of the electrical offset from 25 °C to the considered temperature:

$$I_{OT}(T) = I_{OF}(T) - I_{OF}(25 \,^{\circ}\text{C})$$

<u>Note:</u> the transducer has to be demagnetized prior to the application of the current cycle (for example with a demagnetization tunnel).

Total error

The total error $\varepsilon_{\rm tot}$ is the error at $\pm I_{\rm P\,N}$, relative to the rated value $I_{\rm P\,N}$.

The total error $\varepsilon_{\rm tot}$ at 25 °C includes the electrical offset, the magnetic offset, the sensitivity error and the linearity error, expressed in % of $I_{\rm PN}$.

The formula for total error ε_{tot} is:

$$\varepsilon_{\rm tot} \, {\rm at} \, I_{\rm P} = \, \left(\frac{I_{\rm out} \, {\rm at} \, I_{\rm P}}{S_{\rm N}} - I_{\rm P \, ref} \right) \times \, \frac{100}{I_{\rm P \, N}} \, [{\rm in} \, \% \, {\rm of} \, I_{\rm P \, N}]$$

Error

The error ε is defined as the global error for each measured point at a given primary current without taking into account the electrical offset, expressed in % of $I_{\rm PN}$.

The formula for error ε is:

$$\varepsilon$$
 at $I_{\rm P}$ = $\varepsilon_{\rm tot}$ at $I_{\rm P}$ – $I_{\rm O\,E}$ referred to primary [in % of $I_{\rm P\,N}]$

Delay times

The delay time $t_{\rm D\,10}$ @ 10 % and the delay time $t_{\rm D\,90}$ @ 90 % are shown in figure 9.

Both depend on the primary current di/dt. They are measured at nominal ampere-turns.

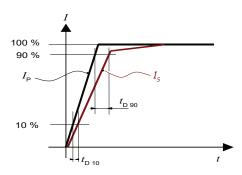


Figure 9: $t_{D,10}$ (delay time @ 10 %) and $t_{D,90}$ (delay time @ 90 %)





Safety

This transducer must be used in limited-energy secondary circuits according to IEC 61010-1.



This transducer must be used in electric/electronic equipment with respect to applicable standards and safety requirements in accordance with the manufacturer's operating instructions.

Caution, risk of electrical shock



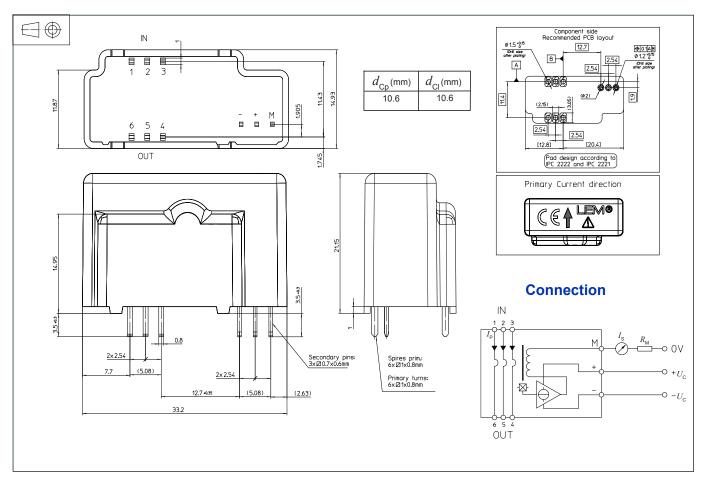
When operating the transducer, certain parts of the module can carry hazardous voltage (e.g. primary busbar, power supply). Ignoring this warning can lead to injury and/or cause serious damage.

This transducer is a build-in device, whose conducting parts must be inaccessible after installation.

A protective housing or additional shield could be used. Main supply must be able to be disconnected.



Dimensions (in mm)



Number of primary turns $N_{\scriptscriptstyle m P}$	Primary nominal RMS current $I_{\scriptscriptstyle{\mathrm{PN}}}$ [A]	Primary current maximum $I_{\scriptscriptstyle m P}$ [A]	Secondary nominal RMS current $I_{\scriptscriptstyle{\mathrm{SN}}}$ [mA]	Turns ratio $N_{ m p}/N_{ m s}$	Resistance of primary winding $R_{_{ m P}}$ [${ m m}\Omega$]	Insertion inductance $L_{_{ m P}}$ [$\mu { m H}$]	Recommended PCB connections
1	25	55	25	1 : 1000	0.18	0.012	3 2 1 IN 0
2	12	27	24	2 : 1000	0.81	0.054	3 2 1 IN 0
3	8	18	24	3 : 1000	1.62	0.110	3 2 1 IN 0 0 0 0 0 OUT 4 5 6

Mechanical characteristics

- General tolerance
- Fastening & connection of primary Recommended PCB hole
- Fastening & connection of secondary Recommended PCB hole

±0.2 mm 6 pins 1 × 0.8 mm 1.5 mm

3 pins 0.7 × 0.6 mm • 1.2 mm

Remarks

- $I_{\rm S}$ is positive when $I_{\rm P}$ flows from terminals 1, 2, 3 (IN) to terminals 6, 5, 4 (OUT).
- The jumper temperature and PCB should not exceed 100 °C.
- This is a standard model. For different versions (supply voltages, turns ratios, unidirectional measuremets...), please contact us.
- Installation of the transducer must be done unless otherwise specified on the datasheet, according to LEM Transducer Generic Mounting Rules. Please refer to LEM document N°ANE120504 available on our Web site: https://www.lem.com/en/file/3137/download/.