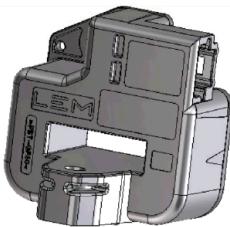


# AUTOMOTIVE CURRENT TRANSDUCER OPEN LOOP TECHNOLOGY

## HAH1DRW 900-S



### Introduction

The HAH1DRW family is for the electronic measurement of DC, AC or pulsed currents in high power and low voltage automotive applications with galvanic separation between the primary circuit (high power) and the secondary circuit (electronic circuit).

The HAH1DRW family gives you the choice of having different current measuring ranges in the same housing.

### Features

- Ratiometric transducer
- Open Loop transducer using the Hall effect
- Low voltage application
- Unipolar +5 V DC power supply
- Primary current measuring range  $\pm 900$  A
- Maximum RMS primary admissible current: defined by busbar to have  $T < +150$  °C
- Operating temperature range:  $-40$  °C  $< T < 125$  °C
- Output voltage: full ratio-metric (in sensitivity and offset).

### Advantages

- Excellent accuracy
- Very good linearity
- Very low thermal offset drift
- Very low thermal sensitivity drift
- Galvanic separation
- High frequency bandwidth
- Non intrusive solution.

### Automotive applications

- Electrical Power Steering
- Starter Generators
- Converters
- Battery Management
- Motor drive application.

### Principle of HAH1DRW Family

The open loop transducers use a Hall effect integrated circuit. The magnetic flux density  $B$ , contributing to the rise of the Hall voltage, is generated by the primary current  $I_P$  to be measured. The current to be measured  $I_P$  is supplied by a current source i.e. battery or generator (Figure 1). Within the linear region of the hysteresis cycle,  $B$  is proportional to:

$$B (I_P) = a \times I_P$$

The Hall voltage is thus expressed by:

$$U_{\text{Hall}} = (c_{\text{Hall}} / d) \times I_{\text{Hall}} \times a \times I_P$$

Except for  $I_P$ , all terms of this equation are constant. Therefore:

$$U_{\text{Hall}} = b \times I_P$$

$a$  constant

$b$  constant

$c_{\text{Hall}}$  Hall coefficient

$d$  thickness of the Hall plate

$I_{\text{Hall}}$  current across Hall plate

The measurement signal  $U_{\text{Hall}}$  is amplified to calculate the user output voltage or current.

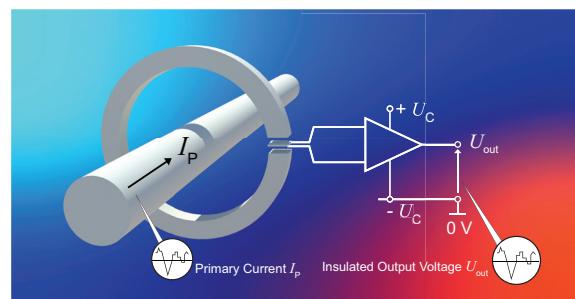
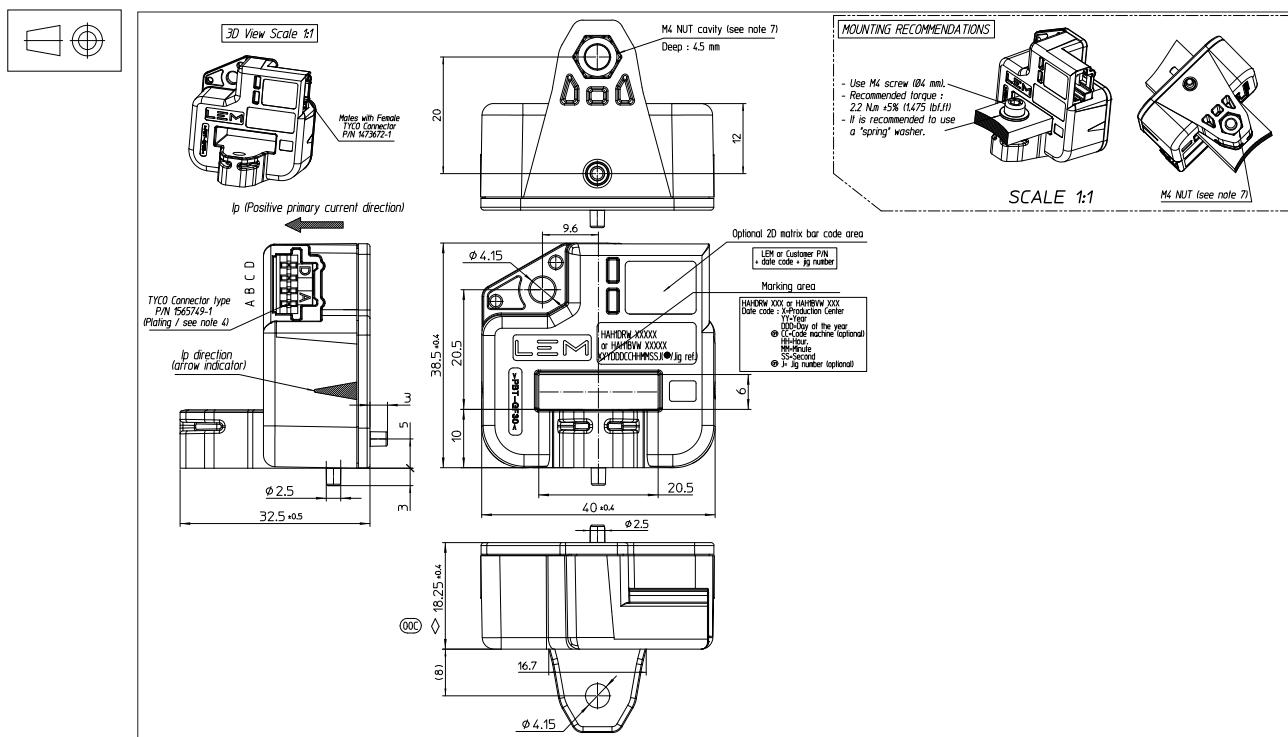


Fig. 1: Principle of the open loop transducer



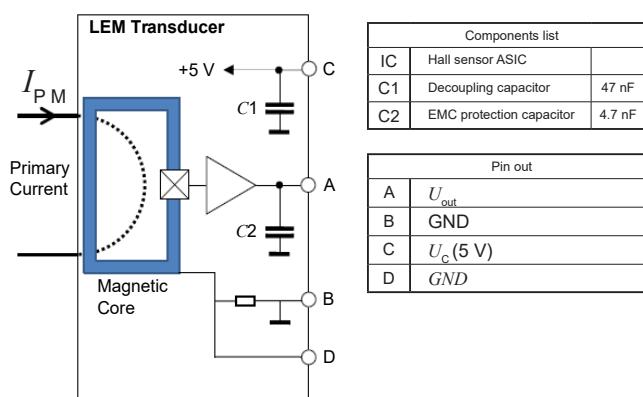
## Mechanical characteristics

- Plastic case PBT GF30
- Magnetic core FeSi wound core
- Mass 57 g  $\pm 5\%$
- Pins Brass tin plated
- IP level IPx2.

## Mounting recommendation

- Mating connector type                    TYCO connector P/N 1473672-1
- Assembly torque max                2.2 N·m  $\pm 5\%$
- Soldering type                            N/A

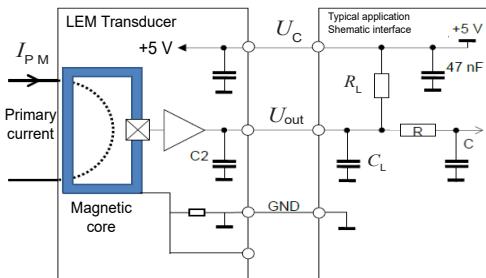
## Electronic schematic



### Remark

- $U_{\text{out}} > U_{\text{o}}$  when  $I_p$  flows in the positive direction (see arrow on drawing).

## System architecture (example)



$C_L < 2.2 \text{ nF}$  EMC protection (optional)  
RC Low pass filter (optional)

## On board diagnostic

$R_L > 10 \text{ k}\Omega$ . Resistor for signal line diagnostic (optional)

$U_{\text{out}}$	Diagnostic
Open circuit	$U_{\text{IN}} = U_{\text{C}}$
Short GND	$U_{\text{...}} = 0 \text{ V}$

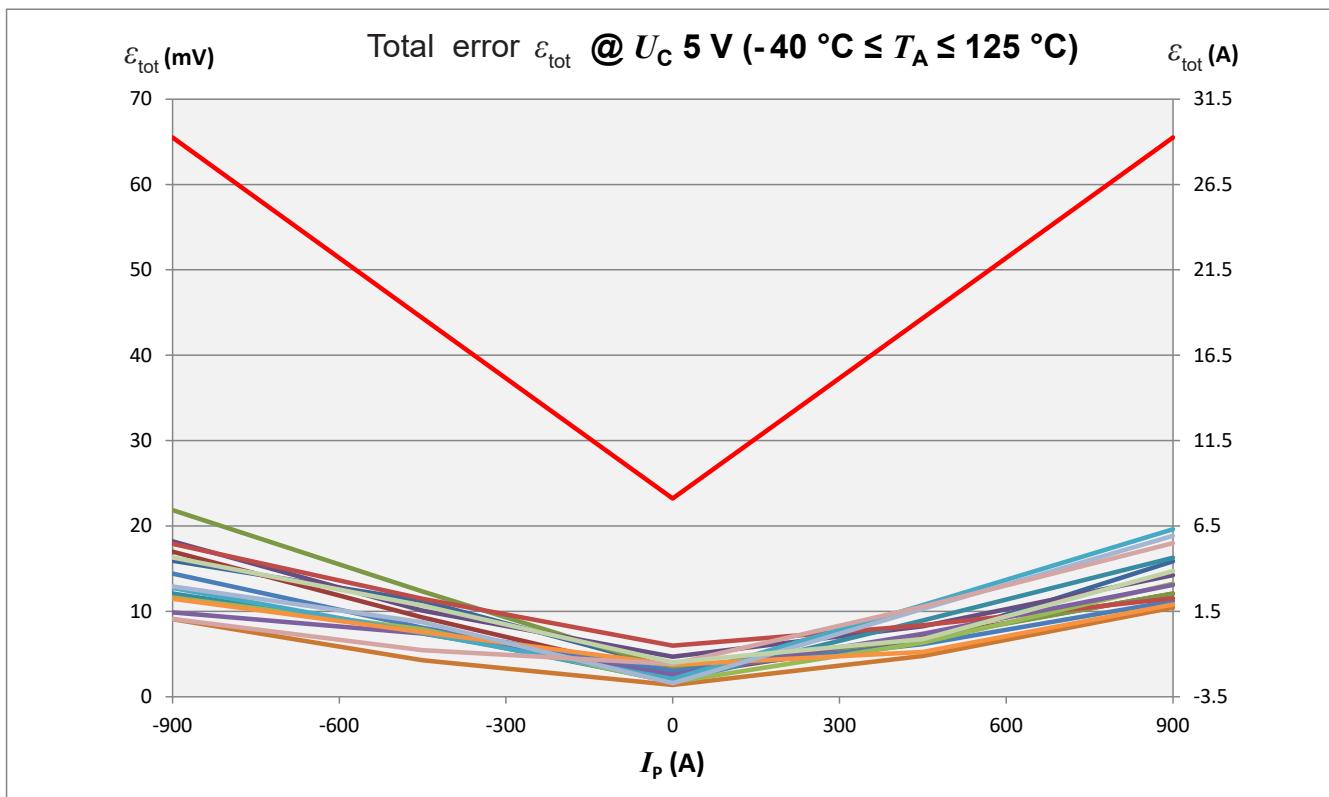
**Absolute ratings (not operating)**

Parameter	Symbol	Unit	Specification			Conditions
			Min	Typical	Max	
Maximum supply voltage	$U_{C\max}$	V	-0.5		8	1)
Ambient storage temperature	$T_{A\text{st}}$	°C	-40		125	
Electrostatic discharge voltage (HBM)	$U_{\text{ESD HBM}}$	kV			8	
Maximum admissible vibration (random RMS)	$\gamma_{\max}$	m/s <sup>2</sup>			27.1	10 to 1000 Hz, -40 °C to 125 °C
RMS voltage for AC insulation test, 50 Hz, 1 min	$U_d$	kV			2.5	50 Hz, 1 min
Creepage distance	$d_{Cp}$	mm	4.85			
Clearance	$d_{Cl}$	mm	4.85			
Comparative tracking index	$CTI$		PLC3			
Maximum output current	$I_{\text{out max}}$	mA	-10		10	
Maximum output voltage	$U_{\text{out max}}$	V	-0.5		$U_c + 0.5$	

**Operating characteristics in nominal range ( $I_{P_N}$ )**

Parameter	Symbol	Unit	Specification			Conditions
			Min	Typical	Max	
<b>Electrical Data</b>						
Primary current, measuring range	$I_{P_M}$	A	-900		900	
Primary nominal RMS current	$I_{P_N}$	A	-900		900	
Supply voltage	$U_c$	V	4.75	5	5.25	
Ambient operating temperature	$T_A$	°C	-40		125	
Output voltage (Analog)	$U_{\text{out}}$	V	$U_{\text{out}} = (U_c/5) \times (U_o + S \times I_p)$			
Sensitivity	$S$	mV/A		2.222		@ $T_A = 25$ °C
Offset voltage	$U_o$	V		2.5		
Current consumption	$I_c$	mA		20	25	
Load resistance	$R_L$	kΩ	10			
Output internal resistance	$R_{\text{out}}$	Ω		1	10	
<b>Performance Data</b>						
Ratiometricity error	$\varepsilon_r$	%		±0.5		
Sensitivity error	$\varepsilon_s$	%		±0.6		@ $T_A = 25$ °C, @ $U_c = 5$ V
Electrical offset voltage	$U_{OE}$	mV		±3		@ $T_A = 25$ °C, @ $U_c = 5$ V
Magnetic offset voltage	$U_{OM}$	mV		±2		@ $U_c = 5$ V, @ $T_A = 25$ °C
Linearity error	$\varepsilon_L$	%	-1		1	% of full scale
Average temperature coefficient of $U_{OE}$	$TCU_{OE\text{AV}}$	mV/°C		±0.04		
Average temperature coefficient of $S$	$TCS_{\text{AV}}$	%/°C		±0.02		
Delay time to 90 % of the final output value for $I_{P_N}$ step	$t_{D\text{90}}$	μs		2	6	$di/dt = 100$ A / μs
Frequency bandwidth	$BW$	kHz	40			@ -3 dB
Peak-to-peak noise voltage	$U_{\text{no pp}}$	mV			14	DC to 1 MHz
RMS noise voltage referred to primary	$U_{\text{no}}$	mV			2.2	
Phase shift	$\Delta\phi$	°	-4			DC to 1 KHz

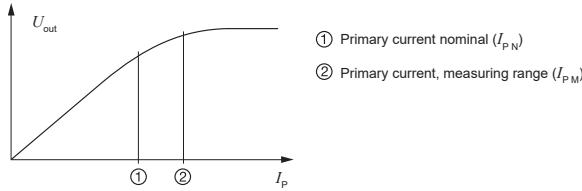
Note: 1) Exceeding 6.5 V may temporarily reconfigure the device until next power on.

Total error  $\varepsilon_{\text{tot}}$ 


$I_P$ (A)	Total Error $\varepsilon_{\text{tot}}$ specification		
	$T_A = 25$ °C, $U_C = 5$ V	$-40$ °C ≤ $T_A$ ≤ 125 °C, $U_C = 5$ V	
-900	45 mV	20.25 A	2.25 %
0	13 mV	5.85 A	0.65 %
900	45 mV	20.25 A	2.25 %

## PERFORMANCES PARAMETERS DEFINITIONS

### Primary current definition:



### Definition of typical, minimum and maximum values:

Minimum and maximum values for specified limiting and safety conditions have to be understood as such as values shown in "typical" graphs. On the other hand, measured values are part of a statistical distribution that can be specified by an interval with upper and lower limits and a probability for measured values to lie within this interval. Unless otherwise stated (e.g. "100 % tested"), the LEM definition for such intervals designated with "min" and "max" is that the probability for values of samples to lie in this interval is 99.73 %. For a normal (Gaussian) distribution, this corresponds to an interval between -3 sigma and +3 sigma. If "typical" values are not obviously mean or average values, those values are defined to delimit intervals with a probability of 68.27 %, corresponding to an interval between -sigma and +sigma for a normal distribution. Typical, minimum and maximum values are determined during the initial characterization of a product.

### Output noise voltage:

The output voltage noise is the result of the noise floor of the Hall elements and the linear amplifier.

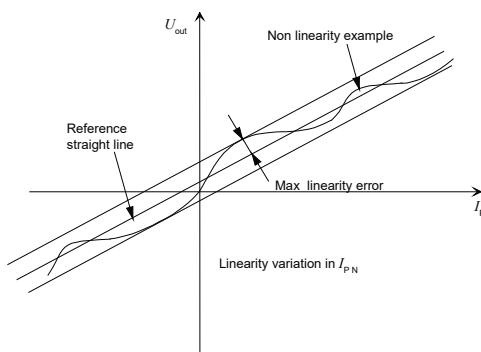
### Magnetic offset:

The magnetic offset is the consequence of any current on the primary side. It's defined after a stated excursion of primary current.

### Linearity:

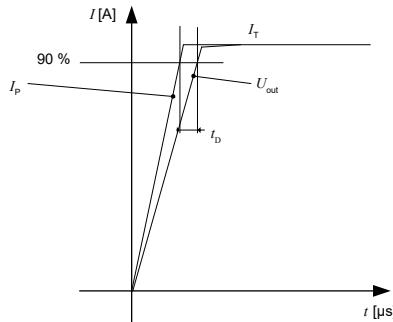
The maximum positive or negative discrepancy with a reference straight line  $U_{out} = f(I_p)$ .

Unit: linearity (%) expressed with full scale of  $I_{pN}$ .



### Delay time $t_{D90}$ :

The time between the primary current signal ( $I_{pN}$ ) and the output signal reach at 90 % of its final value.



### Sensitivity:

The transducer's sensitivity  $S$  is the slope of the straight line  $U_{out} = f(I_p)$ , it must establish the relation:

$$U_{out}(I_p) = U_c/5 (S \times I_p + U_0)$$

### Offset with temperature:

The error of the offset in the operating temperature is the variation of the offset in the temperature considered with the initial offset at 25 °C.

The offset variation  $I_{oT}$  is a maximum variation the offset in the temperature range:

$$I_{oT} = I_{oE} \text{ max} - I_{oE} \text{ min}$$

The offset drift  $TCI_{oEAV}$  is the  $I_{oT}$  value divided by the temperature range.

### Sensitivity with temperature:

The error of the sensitivity in the operating temperature is the relative variation of sensitivity with the temperature considered with the initial offset at 25 °C.

The average temperature coefficient of sensitivity  $TCS_{AV}$   $S_T$  is the maximum temperature variation of  $S$  (in ppm or %) of the sensitivity in the temperature range:

$$S_T = (\text{Sensitivity max} - \text{Sensitivity min}) / \text{Sensitivity at } 25^\circ\text{C}$$

The average temperature coefficient of sensitivity  $TCS_{AV}$  is the  $S_T$  value divided by the temperature range.

Deeper and detailed info is available is in our LEM technical sales offices ([www.lem.com](http://www.lem.com)).

### Offset voltage @ $I_p = 0$ A:

The offset voltage is the output voltage when the primary current is zero. The ideal value of  $U_o$  is  $U_c/2$ . So, the difference of  $U_o - U_c/2$  is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis. Deeper and detailed info is available is in our LEM technical sales offices ([www.lem.com](http://www.lem.com)).

**Validation test specifications:**

Refer to LEM GROUP test plan laboratory CO.11.11.515.0 with "Tracking\_Test Plan\_Auto" sheet.

Name	Standard
<b>CHARACTERIZATION @ 25 °C (initial)</b>	
Sensitivity / Accuracy / Overall accuracy	LEM 98.20.00.574.0
Offset / Electrical Offset / Magnetic Offset	LEM 98.20.00.573.0
Linearity error	LEM 98.20.00.370.0
Current Consumption	LEM 98.20.00.579.0
<b>CHARACTERIZATION WITH T °C (initial)</b>	
Sensitivity / Accuracy / Overall accuracy	LEM 98.20.00.574.0
T °C variation of ... / Temperature Coefficient of S	LEM 98.20.00.574.0
Offset / Electrical Offset / Magnetic Offset	LEM 98.20.00.573.0
T °C variation of ... / Temperature Coefficient of Offset	LEM 98.20.00.573.0
Linearity error	LEM 98.20.00.370.0
Current Consumption	LEM 98.20.00.579.0
<b>ELECTRICAL TESTS @ 25 °C</b>	
Phase delay check	100 Hz to 10 kHz @ 20 A peak
Noise measurement	Sweep from DC to 1 MHz
Delay time di/dt	100 A/μs. I pulse = 700
dv/dt	2000 V/μs. V = 2000 V
Dielectric Withstand Voltage test	2500 V AC / 1 min / 50 Hz
Insulation Resistance test	500 V DC, time = 60 s $R_{INS} \geq 500 \text{ M}\Omega$ Minimum
<b>ENVIRONMENTAL TESTS (CLIMATIC)</b>	
Thermal shock	IEC 60068-2-14 Na (01/2009)
Steady state T °C Humidity bias life test	JESD 22-A101 (03/2009)
<b>MECHANICAL TESTS</b>	
Vibration Random in T °C	IEC 60068-2-64 (02/2008)
Shocks	IEC 60068-2-27 (02/2008)
Free Fall (Device not packaged)	IEC 60068-2-31 §5.2: method 1 (05/2008)
<b>EMC</b>	
Immunity to ElectroStatic Discharges (Handling of devices)	ISO 10605 (07/2008)
Immunity to Conducted disturbances (BCI)	ISO 11452-4 (12/2011)
Emission Radiated (ALSE)	CISPR 25 (03/2008)
<b>FINAL CHARACTERIZATION</b>	
Characterization @ 25 °C	
Characterization with T °C	