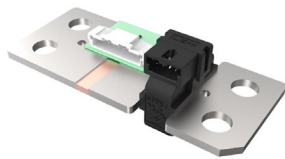


# AUTOMOTIVE HYBRID CURRENT TRANSDUCER SHUNT AND OPEN LOOP HALL TECHNOLOGY HSU03



## Introduction

The HSU is a hybrid current transducer for DC & AC measurement of the battery packs in electric and hybrid vehicles

HSU has 2 different current measurement channels: Shunt and hall effect.

## Features

- High insulation level
- Unipolar +5 V DC power supply for HALL
- Primary current measuring range up to  $\pm 2000$  A (10 s) both Shunt and Hall
- Maximum RMS primary admissible current: defined by the busbar, the magnetic core or ASIC  $T < +125$  °C
- Operating temperature range:  $-40$  °C  $< T < +125$  °C
- Output voltage: fully ratio-metric (in sensitivity and offset) for Hall effect channel.

## Advantages

- Excellent accuracy
- Very good linearity
- Very low thermal offset drift
- Very low thermal sensitivity drift.

## Automotive applications

- Battery management
- EV, Hybrid and utility vehicles
- The HSU Family is designed to run in a vehicle battery pack or in a Battery Disconnect Unit and cannot be used in an environment exposed to water projections or gravel projections. The HSU is compliant with Functional Safety standard ISO 26262. The test items used to validate the product are described at the end of the document
- The test items used to validate the product are described at the end of the document.

## Principle of HSU - Shunt channel

There'll be a voltage drop when current goes through shunt, and current can be deduced by Ohm's Law,  $I = U / R_{\text{Shunt}}$ .

## Principle of HSU - Hall effect channel

The open loop transducers use a Hall effect integrated circuit. The magnetic flux density  $B$ , contributing to the rise of the Hall voltage, is generated by the primary current  $I_p$  to be measured.

The current to be measured  $I_p$  is supplied by a current source i.e. battery or generator (Figure 1).

Within the linear region of the hysteresis cycle,  $B$  is proportional to:

$$B(I_p) = a \times I_p$$

The Hall voltage is thus expressed by:

$$U_{\text{Hall}} = (c_{\text{Hall}} / d) \times I_{\text{Hall}} \times a \times I_p$$

Except for  $I_p$ , all terms of this equation are constant. Therefore:

$$U_{\text{Hall}} = b \times I_p$$

$a$  constant

$b$  constant

$c_{\text{Hall}}$  Hall coefficient

$d$  thickness of the Hall plate

$I_{\text{Hall}}$  current across the Hall plate

The measurement signal  $U_{\text{Hall}}$  is amplified to calculate the user output voltage or current.

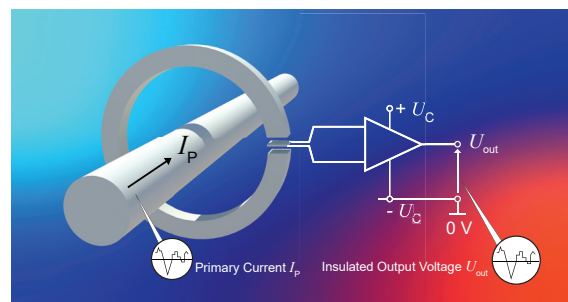
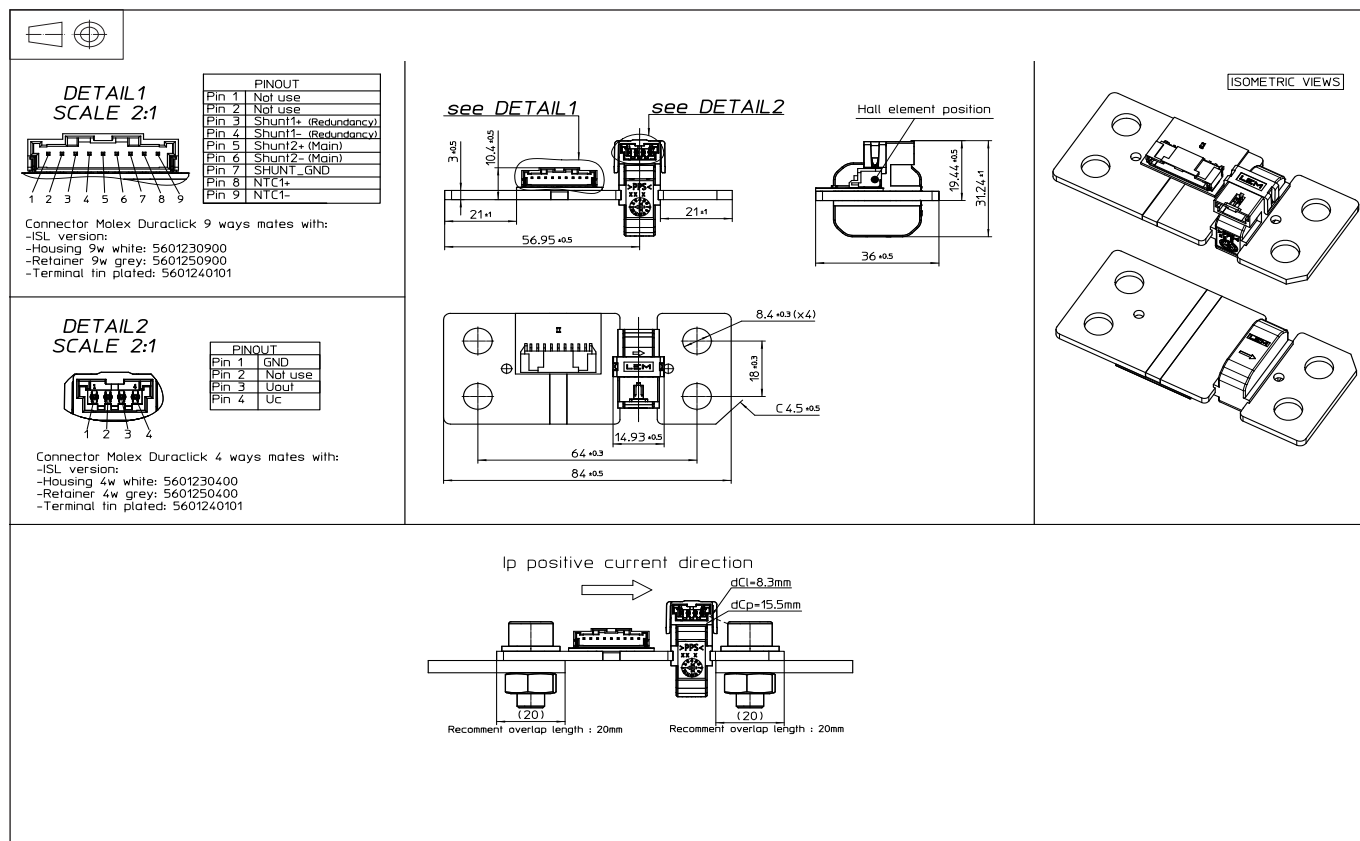


Fig. 1: Principle of the open loop transducer



## Mechanical characteristics

- Mass 92 g ( $\pm 5\%$ ).

### Mounting recommendation

- Assembly torque M8 Screw with 10 – 15 N·m  
M6 Screw with 8 – 10 N·m.

**Note:** The installation planar should have a flatness tolerance of  $\leq 0.2$  mm. The size tolerances of the installation bracket and nut, as well as the use of installation tools and fixtures, have an impact on the maximum installation torque of the product. If there are any relevant design or process changes from customer side, it is advisable to implement them after verification.

### Remark

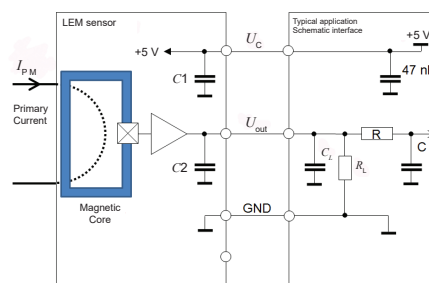
- $U_{\text{out}} > U_{\text{O}}$  when  $I_{\text{P}}$  flows in the positive direction (see arrow on drawing).

$$T = 1 / \left( \ln \left( \frac{R_{NTC}}{10000} \right) / B + \frac{1}{273.15 + 25} \right) - 273.15$$

**Notes:**

- 1) NTC type: NCU18XH103F6SRB
- 2) Main NTC is measured in Pin8 and Pin9  
Redundancy NTC is measured in Pin1 and Pin2, currently reserved.

## System architecture (Hall)

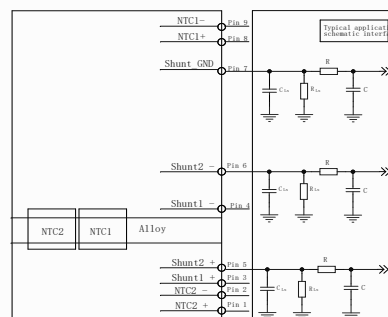


$C_1 < 100 \text{ nF}$  EMC protection (optional)

$$R_i \geq 10k \text{ Ohm}$$

RC Low pass filter (optional)

## System architecture (Shunt)



$C_{LS}$  &  $R_{LS}$  Value is based on system design(optional)  
RC Low pass filter(Optional)

**Absolute ratings (not operating)**

| Parameter                                                | Symbol               | Unit | Specification |         |     | Conditions                    |
|----------------------------------------------------------|----------------------|------|---------------|---------|-----|-------------------------------|
|                                                          |                      |      | Min           | Typical | Max |                               |
| Maximum supply voltage                                   | $U_{C\max}$          | V    | -14           |         | 14  | Continuous, not operating     |
| Ambient storage temperature                              | $T_{A\text{st}}$     | °C   | -40           |         | 125 |                               |
| Electrostatic discharge voltage (HBM - Human Body Model) | $U_{\text{ESD HBM}}$ | kV   |               |         | 8   |                               |
| RMS voltage for AC insulation test                       | $U_d$                | kV   |               |         | 4.2 | 50 HZ, 1 min, IEC 60664 part1 |
| Creepage distance                                        | $d_{\text{Cp}}$      | mm   | 15.5          |         |     |                               |
| Clearance                                                | $d_{\text{Cl}}$      | mm   | 8.3           |         |     |                               |
| Comparative tracking index                               | $CTI$                | -    | PLC3          |         |     |                               |
| Maximum output current                                   | $I_{\text{out max}}$ | mA   | -10           |         | 10  |                               |
| Maximum output voltage                                   | $U_{\text{out max}}$ | V    | -14           |         | 14  |                               |
| Insulation resistance                                    | $R_{\text{INS}}$     | MΩ   | 500           |         |     |                               |

**Operating characteristics in nominal range ( $I_{PN}$ ) For SHUNT**

| Parameter                             | Symbol | Unit   | Specification |         |       | Conditions               |
|---------------------------------------|--------|--------|---------------|---------|-------|--------------------------|
|                                       |        |        | Min           | Typical | Max   |                          |
| Resistance value                      | $R$    | μΩ     | 23.75         |         | 26.25 | @ 25 °C                  |
| Temperature coefficient of resistance | $a_R$  | PPM/°C | -200          |         | 200   | @ 25 ~ 125 °C            |
| Operating temperature                 | $T$    | °C     | -40           |         | 125   | Based on NTC temperature |

**Note:** <sup>1)</sup> Definition of resistance tolerance is measured between Pin5 and Pin6  
Resistance value between Pin3 and Pin4 is for reference only.

**Operating characteristics in nominal range ( $I_{PN}$ ) FOR HALL**

| Parameter                                   | Symbol          | Unit  | Specification                                     |         |      | Conditions                                                           |
|---------------------------------------------|-----------------|-------|---------------------------------------------------|---------|------|----------------------------------------------------------------------|
|                                             |                 |       | Min                                               | Typical | Max  |                                                                      |
| Electrical Data                             |                 |       |                                                   |         |      |                                                                      |
| Supply voltage                              | $U_C$           | V     | 4.75                                              | 5       | 5.25 |                                                                      |
| Ambient operating temperature               | $T_A$           | °C    | -40                                               | -       | 125  |                                                                      |
| Output voltage (Analog) <sup>1)</sup>       | $U_{out}$       | V     | $U_{out} = (U_C / 5) \times (U_O + S \times I_P)$ |         |      |                                                                      |
| Offset voltage                              | $U_O$           | V     |                                                   | 2.5     |      | @ $U_C = 5\text{ V}$                                                 |
| Current consumption                         | $I_C$           | mA    |                                                   | 15      |      | @ $U_C = 5\text{ V}$ , @ $T_A = 25\text{ °C}$ , @ $I_P = 0\text{ A}$ |
| Load resistance                             | $R_L$           | KΩ    | 10                                                |         |      |                                                                      |
| Output internal resistance                  | $R_{out}$       | Ω     |                                                   | 1       | 10   |                                                                      |
| Performance Data                            |                 |       |                                                   |         |      |                                                                      |
| Ratiometricity error                        | $\varepsilon_r$ | %     |                                                   | ±0.3    |      | @ $T_A = 25\text{ °C}$                                               |
| Sensitivity error                           | $\varepsilon_S$ | %     |                                                   | ±1      |      | @ $T_A = 25\text{ °C}$ , @ $U_C = 5\text{ V}$                        |
| Electrical offset voltage                   | $U_{OE}$        | mV    |                                                   | ±3      |      | @ $T_A = 25\text{ °C}$ , @ $U_C = 5\text{ V}$                        |
| Magnetic offset voltage                     | $U_{OM}$        | mV    |                                                   | ±1      |      | @ $T_A = 25\text{ °C}$ , @ $U_C = 5\text{ V}$                        |
| Average temperature coefficient of $U_{OE}$ | $TCU_{OEAV}$    | mV/°C |                                                   | ±0.04   |      | @ $-40\text{ °C} < T_A < 125\text{ °C}$                              |
| Average temperature coefficient of $S$      | $TCS_{AV}$      | %/°C  | -0.03                                             | ±0.01   | 0.03 | @ $-40\text{ °C} < T_A < 125\text{ °C}$                              |
| Linearity error                             | $\varepsilon_L$ |       |                                                   | ±1      |      | @ $U_C = 5\text{ V}$ , @ $T_A = 25\text{ °C}$                        |
| Frequency bandwidth <sup>2)</sup>           | $BW$            | Hz    |                                                   | 35      |      | @ -3 dB                                                              |
| Step response time @ 90%                    | $t_r$           | ms    |                                                   | 5       |      | @ 100A/us                                                            |
| Peak-to-peak noise voltage                  | $U_{no\ pp}$    | mV    |                                                   | 10      |      | @ DC to 1 MHz                                                        |
| Output RMS noise voltage                    | $U_{no}$        | mV    |                                                   | 1.5     |      | @ DC to 1 MHz                                                        |
| Start-up time                               | $t_{start}$     | ms    |                                                   |         | 1    |                                                                      |

**Notes:** <sup>1)</sup> The output voltage  $U_{\text{out}}$  is fully ratiometric. The offset and sensitivity are dependent on the supply voltage  $U_C$  relative to the following formula:

$$I_P = \left( \frac{5}{U_C} \times U_{\text{out}} - U_O \right) \times \frac{1}{S} \text{ with } S \text{ in (V/A)}$$

<sup>2)</sup> Primary current frequencies must be limited in order to avoid excessive heating of the busbar, magnetic core and the ASIC  
(see feature paragraph in page 1).

**SHUNT**

| Parameter                        | Symbol   | Unit | Specification |         |      | Conditions      |
|----------------------------------|----------|------|---------------|---------|------|-----------------|
|                                  |          |      | Min           | Typical | Max  |                 |
| Electrical Data                  |          |      |               |         |      |                 |
| Primary current, measuring range | $I_{PM}$ | A    | −2000         |         | 2000 | Duration ≤ 10 s |
| Primary nominal RMS current      | $I_{PN}$ | A    | −500          |         | 500  |                 |

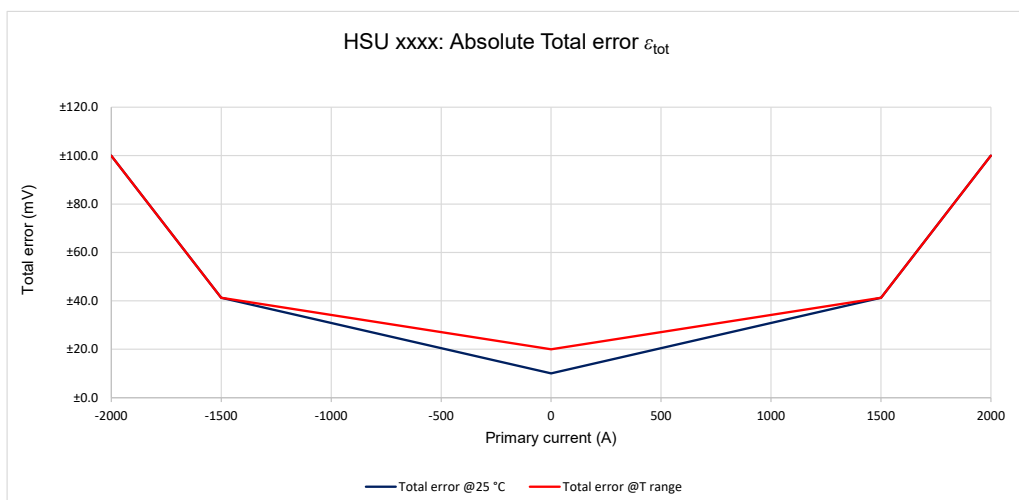
**HALL**

| Parameter                        | Symbol   | Unit | Specification |         |      | Conditions           |
|----------------------------------|----------|------|---------------|---------|------|----------------------|
|                                  |          |      | Min           | Typical | Max  |                      |
| Electrical Data                  |          |      |               |         |      |                      |
| Primary current, measuring range | $I_{PM}$ | A    | -2000         |         | 2000 |                      |
| Sensitivity                      | $S$      | mV/A |               | 1       |      | @ $U_c = 5\text{ V}$ |

**Care note for storage**

The shunt current module shall be stored in an environment where temperature and humidity must be controlled (temperature 5 °C to 35 °C, humidity < 60 % RH) . However, the humidity should be maintained as low as possible.

## Total error $\varepsilon_{\text{tot}}$ HALL



| Primary current | Total error @ 25 °C |       |                   | Total error @ T range |       |                   |
|-----------------|---------------------|-------|-------------------|-----------------------|-------|-------------------|
| (A)             | (mV)                | A     | (%) $I_{PM}^{1)}$ | (mV)                  | (A)   | (%) $I_{PM}^{1)}$ |
| -2000           | 100                 | 100   | 5.00 %            | 100                   | 100   | 5.00 %            |
| -1500           | 41.25               | 41.25 | 2.06 %            | 41.25                 | 41.25 | 2.06 %            |
| 0               | 10                  | 10    | -                 | 20                    | 20    | -                 |
| 1500            | 41.25               | 41.25 | 2.06 %            | 41.25                 | 41.25 | 2.06 %            |
| 2000            | 100                 | 100   | 5.00 %            | 100                   | 100   | 5.00 %            |

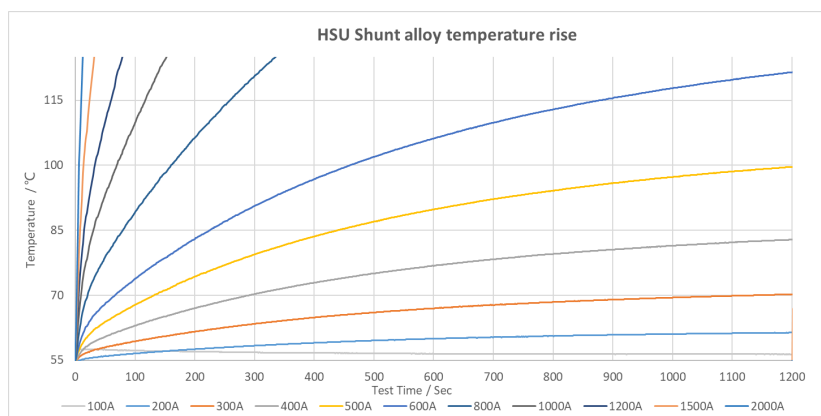
**Note:** <sup>1)</sup> Hall channel accuracy calculated by  $I_{PM}$ .

## Derating curve

### Setup

|                                           |                        |
|-------------------------------------------|------------------------|
| Condition                                 | HSU:                   |
| - Inside climate chamber                  | - Cu-OF                |
| - 25 °C setting up in chamber             | - Section: 3 x 36 (mm) |
| - Test cable: copper(150mm <sup>2</sup> ) | - Length: 84 mm        |
| - Test points: alloy                      |                        |

### Heating generation chart according to above condition

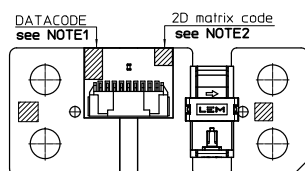


Notes: Depending on the conditions of use, the temperature of the exposed parts of Shunt alloy may reach or exceed 125°C.

### DATECODE & 2D Matrix Code

#### NOTE1

DATECODE: PYYDDCCCHMMSSJ  
 XXXXX: PRODUCT NAME  
 P= Product center  
 YY= Year  
 DDD= Day of Year  
 CC= Code machine  
 HH= Hour  
 MM= Minute  
 SS= Second  
 J= Jig number

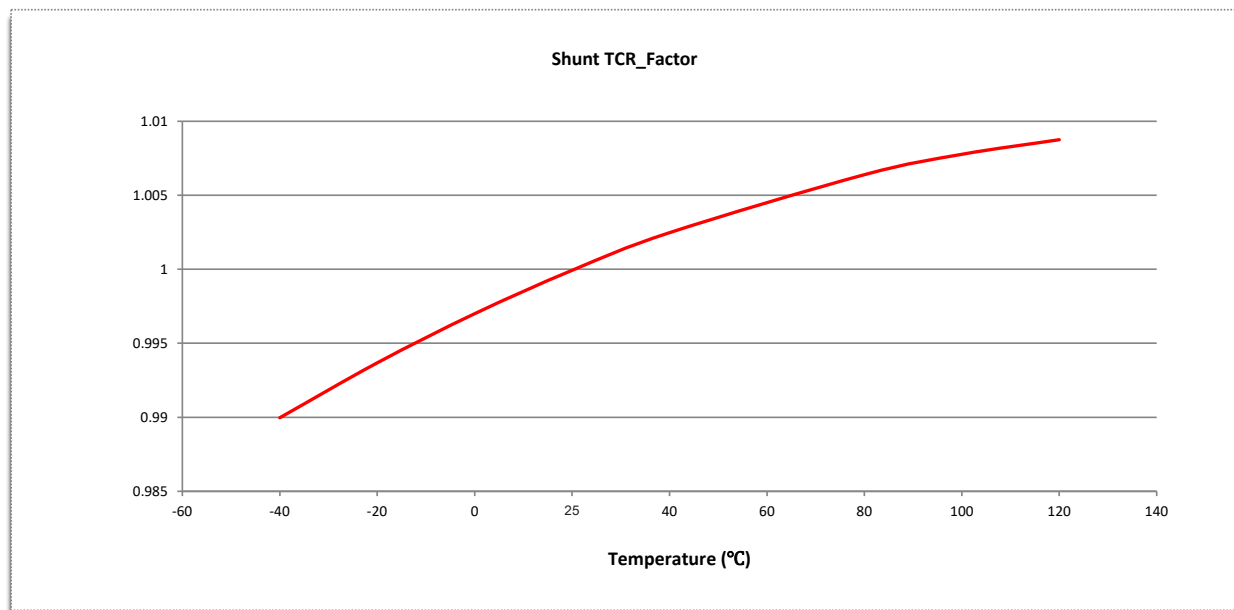


#### NOTE2

|              |                                      |               |
|--------------|--------------------------------------|---------------|
| Module Info. | PCBA Part No(11 digits)              | xxxxxxxxxxx   |
|              | Version(3 digits)                    | xxx           |
|              | Production date(6 digits)            | YYMMDD        |
|              | Production Number(5 digits)          | XXXXX         |
| Shunt Info.  | Year(4 digits)                       | YYYY          |
|              | Month(2 digits)                      | MM            |
|              | Day(2 digits)                        | DD            |
|              | Module ID(5 digits)                  | XXXXX         |
|              | Resistance R25(7 digits)             | Rxxxxxn       |
|              | A:Quadratic coefficient(12 digits)   | +x.xxxxxxxxxx |
|              | B:First-order coefficient(12 digits) | +x.xxxxxxxxxx |
|              | C:Constant(12 digits)                | +x.xxxxxxxxxx |

\* R25 is shunt resistance at 25°C, unit: mΩ  
 \* Font: Arial

## Shunt Temperature Compensation information



Generic compensation factor for resistance of shunt need to be multiplied with:

$$R_{temp} = R_{25} * (A * T^2 + B * T + C).$$

Where:

$R_{temp}$  is the compensated from the factor for shunt resistance drift over temperature normalized to 1 at 25 °C.

$T$  is temperature reading from PCB temperature transducer NTC.

$A$  is quadratic coefficient, the default value is -0.xxxxxxxxxx.

$B$  is first-order coefficient, the default value is +0.xxxxxxxxxx.

$C$  constant term coefficient, the default value is +0.xxxxxxxxxx.

$R_{25}$  is resistance value of shunt at 25 °C.

## Functional Safety

Functional Safety Functional Safety Top Level Safety Requirement

HSU (Hall effect channel) – SR1 Current sensor error

| Item1                | Definitions                                                                                                                                                                                                                                                                                                                                           |
|----------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| LEM Description      | The sensor shall provide a valid current measurement value when the error versus the target value is lower than 10 %. The sensor accuracy error is below 5 % in the current ranges: [-2000 A; -1500 A [ and ] +1500 A; +2000 A]; in the current range [-1500 A; +1500 A] with an error below 2.75 %, and with offset less than 20 mV when $I_p = 0$ . |
| ASIL                 | A(D)                                                                                                                                                                                                                                                                                                                                                  |
| Operation Conditions | Running (when sensor is supplied)                                                                                                                                                                                                                                                                                                                     |
| FHTI                 | Less than 20ms <sup>(1)</sup>                                                                                                                                                                                                                                                                                                                         |
| Safe state           | No sensor internal safe state for accuracy error only for internal integrity failure <sup>(2)</sup>                                                                                                                                                                                                                                                   |

(1) Detailed Note for FHTI:

5ms in case of Power supply failure

Immediate in case of Memory failure

20ms in case of Broken Supply Wire or ADC Output Clipping

(2) Note for Safe state

**Power supply failure:** Sensor Output value set to GND (external pull-down resistor required)

**Memory Failure:** Sensor output value goes to  $(0 \sim 0.2)\% * U_C$  or  $(96 \sim 100)\% * U_C$ .

[ $U_C$  shall be within [4.75V, 5.25V].]

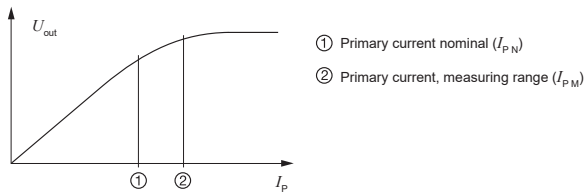
**Broken Supply Wire:** Sensor output goes to  $V = (500nA \text{ Max}) * R_L$ . [ $R_L$  shall be no less than 10Kohm.][All temperature range with  $U_C = 5V$ ]

**ADC Output Clipping:** Sensor output value goes to  $(0 \sim 0.2)\% * U_C$  or  $(96 \sim 100)\% * U_C$ .

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## PERFORMANCES PARAMETERS DEFINITIONS

### Primary current definition:



### Definition of typical, minimum and maximum values:

Minimum and maximum values for specified limiting and safety conditions have to be understood as such as values shown in "typical" graphs. On the other hand, measured values are part of a statistical distribution that can be specified by an interval with upper and lower limits and a probability for measured values to lie within this interval. Unless otherwise stated (e.g. "100 % tested"), the LEM definition for such intervals designated with "min" and "max" is that the probability for values of samples to lie in this interval is 99.73 %. For a normal (Gaussian) distribution, this corresponds to an interval between  $-3$  sigma and  $+3$  sigma. If "typical" values are not obviously mean or average values, those values are defined to delimit intervals with a probability of 68.27 %, corresponding to an interval between  $-\text{sigma}$  and  $+\text{sigma}$  for a normal distribution. Typical, minimum and maximum values are determined during the initial characterization of a product.

### Output noise voltage:

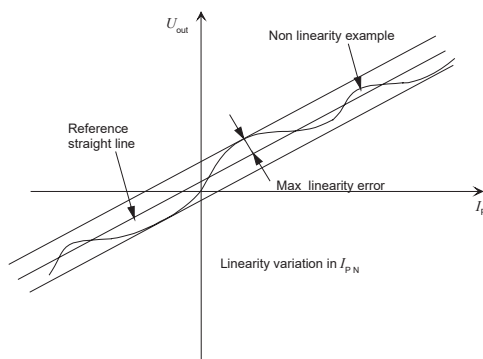
The output voltage noise is the result of the noise floor of the Hall elements and the linear amplifier.

### Magnetic offset:

The magnetic offset is the consequence of an any current on the primary side. It's defined after a stated excursion of primary current.

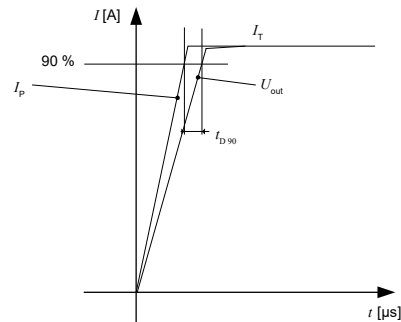
### Linearity:

The maximum positive or negative discrepancy with a reference straight line  $U_{out} = f(I_p)$ .  
Unit: linearity (%) expressed with full scale of  $I_{pN}$ .



### Delay time $t_{D90}$ :

The time between the primary current signal ( $I_{pN}$ ) and the output signal reach at 90 % of its final value.



### Sensitivity:

The transducer's sensitivity  $S$  is the slope of the straight line  $U_{out} = f(I_p)$ , it must establish the relation:

$$U_{out}(I_p) = U_c / 5 (S \times I_p + U_o)$$

### Offset with temperature:

The error of the offset in the operating temperature is the variation of the offset in the temperature considered with the initial offset at 25 °C.

The offset variation  $I_{OT}$  is a maximum variation the offset in the temperature range:

$$I_{OT} = I_{OE \max} - I_{OE \min}$$

The offset drift  $TCI_{OE \text{ AV}}$  is the  $I_{OT}$  value divided by the temperature range.

### Sensitivity with temperature:

The error of the sensitivity in the operating temperature is the relative variation of sensitivity with the temperature considered with the initial offset at 25 °C.

The sensitivity variation  $S_T$  is the maximum variation (in ppm or %) of the sensitivity in the temperature range:  
 $S_T = (\text{Sensitivity max} - \text{Sensitivity min}) / \text{Sensitivity at } 25^\circ \text{C}$ .

The sensitivity drift  $TCS_{AV}$  is the  $S_T$  value divided by the temperature range. Deeper and detailed info available is in our LEM technical sales offices ([www.lem.com](http://www.lem.com)).

### Offset voltage @ $I_p = 0 \text{ A}$ :

The offset voltage is the output voltage when the primary current is zero. The ideal value of  $U_o$  is  $U_c / 2$ . So, the difference of  $U_o - U_c / 2$  is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis. Deeper and detailed info available is in our LEM technical sales offices ([www.lem.com](http://www.lem.com)).

**Verification Plan**

| Tests                                                         | Standard                           | Specific Conditions                                                                                                                                                                                                                                      |
|---------------------------------------------------------------|------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>ELECTRICAL PERFORMANCES</b>                                |                                    |                                                                                                                                                                                                                                                          |
| Frequency bandwidth                                           |                                    | $U_C = 5 \text{ V}$ , 30 Hz to 100 kHz; At 20 A peak                                                                                                                                                                                                     |
| <b>ENVIRONMENTAL TESTS (Climatic)</b>                         |                                    |                                                                                                                                                                                                                                                          |
| Ageing 85 °C /85 % RH                                         | JESD 22-A101<br>(03/2009)          | $T = 85 \text{ °C}$ ; RH = 85 %; Duration = 1000 h<br>$U_C = 5 \text{ V}$ ; $I_p = 0 \text{ A}$ ; Monitoring each 60 min                                                                                                                                 |
| Low temperature storage                                       | ISO 16750-4 § 5.1.1.1<br>(04/2010) | $T = -40 \text{ °C}$<br>Duration = 500 h                                                                                                                                                                                                                 |
| High temperature storage                                      | ISO 16750-4 § 5.1.2.1<br>(04/2010) | $T = 125 \text{ °C}$<br>Duration = 1000 h                                                                                                                                                                                                                |
| Power temperature cycle test                                  | ISO 16750-4 § 5.3.1<br>(04/2010)   | 30 cycles (240 h), $-40 \text{ °C}$ / $125 \text{ °C}$<br>Shunt: $I_p = 600 \text{ A}$ ( 30min each test,containe 10S))<br>Hall: $U_C = 5 \text{ V}$ , monitoring $U_{out}$ @ $0 \text{ A}$ ( 30min each test))                                          |
| Thermal shock                                                 | ISO 16750-4 § 5.3.2<br>(04/2010)   | $T = -40 \text{ °C}$ & $125 \text{ °C}$ Duration = 300 cycles 30 min/30 min<br>$U_C = \text{NO}$ power supply ( unconnected) and No wiring harness                                                                                                       |
| Frost test                                                    | IEC 60068-2-30(2005)               | Heat at $-20 \text{ °C}$ for 2H, transfer to 45 feeding degree 95 % humidity 2h5min, and then transfer to $-20 \text{ °C}$                                                                                                                               |
| Salt spray                                                    | GB/T 10125 -2021                   | Temperature condition: 35 °C<br>Solution concentration: 5 %;<br>Test duration: 96h                                                                                                                                                                       |
| <b>ENVIRONMENTAL TESTS (Mechanical)</b>                       |                                    |                                                                                                                                                                                                                                                          |
| Random Vibration                                              | ISO 16750-3 § 4.1.x<br>(12/2012)   | Temperature $-40 \text{ °C}$ / $125 \text{ °C}$<br>RMS acceleration 27.1 m/s <sup>2</sup> , 10 to 1000 Hz                                                                                                                                                |
| Mechanical Shocks                                             | ISO 16750-3 § 4.2<br>(12/2012)     | Pulse shape: half sine, 50 G, 6 ms<br>10 shocks per direction (total 60)                                                                                                                                                                                 |
| <b>SAFETY - Insulation tests</b>                              |                                    |                                                                                                                                                                                                                                                          |
| Insulation test                                               | ISO 16750-2 § 4.11<br>(11/2012)    | $T_A = 25 \text{ °C}$ $U_d = 4.26 \text{ kV RMS}$ (50 Hz ) for 60 s                                                                                                                                                                                      |
| Insulation resistance                                         | ISO 16750-2 § 4.12<br>(12/2012)    | DC = 1000 V RMS (50 Hz ) for 60 s<br>Criteria: $\geq 500 \text{ M Ohm}$                                                                                                                                                                                  |
| <b>EMC TESTS</b>                                              |                                    |                                                                                                                                                                                                                                                          |
| Immunity to Electrostatic Discharges<br>(Handling of devices) | ISO 10605<br>(07/2008)             | Contact discharges: $\pm 4 \text{ kV}$ ; Air discharges: $\pm 8 \text{ kV}$<br>$U_C = 0$                                                                                                                                                                 |
| Immunity to Radiated disturbances<br>(ALSE)                   | ISO 11452-2<br>(2019)              | F = 400 MHz to 1 GHz; Level = 100 V/m (CW, AM 80 %)<br>F = 0.8 GHz to 1GHz; Level = 70 V/m (PM1)<br>F = 1 GHz to 1.2 GHz; Level = 70 V/m (CW, PM1)<br>F = 1.2 GHz to 1.4 GHz; Level = 70 V/m (CW, PM2)<br>F = 1.4 GHz to 2 GHz; Level = 70 V/m (CW, PM1) |
| Immunity to Conducted disturbances<br>(BCI)                   | ISO 11452-4<br>(12/2020)           | $f = 1 \text{ MHz}$ to 400 MHz                                                                                                                                                                                                                           |
| Emission Radiated<br>(ALSE)                                   | CISPR 25 § 6.5<br>(2021)           | $f = 150 \text{ kHz}$ to 6 GHz                                                                                                                                                                                                                           |

## IMPORTANT NOTICE

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